## iC-PG miniature Reflective OPTICAL ABSOLUTE ENCODER

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## FEATURES

- Very compact lens-less opto encoder
- Suits reflective code discs of $\varnothing 8 \mathrm{~mm}$
- Integrated blue LED with power control, EncoderBlue ${ }^{\circledR}$
- Push-button functionality: 64-step bounce-free analog output from 0.5 V to 4.5 V
- Parallel 5-bit Gray code output with 1.6 mA push-pull drivers
- 30 angle positions, resolution of $12^{\circ}$
- 2 positions reserved for PowerOn/Error indication
- Single-sided 5 V supply
- Operating temperature of $-40^{\circ} \mathrm{C}$ to $+105^{\circ} \mathrm{C}$


## APPLICATIONS

- Rotary control knob
- Human-machine interface
- AV equipment
- Electronic potentiometer


## PACKAGES



8-pin optoDFN
$3 \mathrm{~mm} \times 3 \mathrm{~mm} \times 0.9 \mathrm{~mm}$
RoHS compliant

## BLOCK DIAGRAM



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## DESCRIPTION

iC-PG is an easy-to-use optical-reflective absolute encoder featuring integrated photosensors and an integrated blue LED chip.

Its typical applications are rotary knobs and electronic potentiometers.

The device requires a single 5 V supply voltage without any need for configuration.

Push-button functionality is realized by an analog 0.5 V to 4.5 V output representing the distance between chip and code disc.

The device provides a capped 5-bit Gray-coded parallel output signal with 30 angle positions and a resolution of 12 degrees.

The remaining 2 codes are reserved for error indications like power down, low contrast and read error.

General notice on materials under excessive conditions
Epoxy resins (such as solder resists, IC package and injection molding materials, as well as adhesives) may show discoloration, yellowing, and surface changes in general when exposed longterm to high temperatures, humidity, irradiation, or due to thermal treatments for soldering and other manufacturing processes.

Equally, standard molding materials used for IC packages can show visible changes induced by irradiation, among others when exposed to light of shorter wavelengths, blue light for instance. Such surface effects caused by visible or IR LED light are rated to be of cosmetic nature, without influence to the chip's function, its specifications and reliability.

Note that any other material used in the system (e.g. varnish, glue, code disc) should also be verified for irradiation effects.

## PACKAGING INFORMATION

## PIN CONFIGURATION



## PIN FUNCTIONS

## No. Name Function

1 G3 Gray-coded output
2 G2 Gray-coded output
3 G1 Gray-coded output
4 G0 Gray-coded output
5 PB Push-button analog output
6 GND Ground
7 VDD +4.5V...+5.5V Supply Voltage
8 G4 Gray-coded output

PACKAGE DIMENSIONS


All dimensions given in mm. General tolerances of form and position according to JEDEC MO-229.
Positional tolerance of sensor pattern: $\pm 70 \mu \mathrm{~m} / \pm 1^{\circ}$ (with respect to center of backside pad).
Maximum molding excess $+20 \mu \mathrm{~m} /-75 \mu \mathrm{~m}$ versus surface of glass. Small pits in the mold surface, which may occasionally appear due to the manufacturing process, are cosmetic in nature and do not affect reliability.

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## ABSOLUTE MAXIMUM RATINGS

Beyond these values damage may occur; device operation is not guaranteed.

| Item No. | Symbol | Parameter | Conditions | Min. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| G001 | VDD | Voltage at VDD |  | -0.3 | 7 | V |
| G002 | I(VDD) | Current in VDD |  |  |  |  |
| G003 | V() | Pin Voltage, all other pins |  | -0.3 | VDD + 0.3 | V |
| G004 | 1() | Pin Current, all other pins |  |  |  |  |
| G005 | Vd() | ESD Susceptibility, all pins |  |  |  |  |
| G006 | Tj | Junction Temperature |  | -40 | 125 | ${ }^{\circ} \mathrm{C}$ |

THERMAL DATA

| Item No. | Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| T01 | Ta | Operating Ambient Temperature Range |  | -40 |  | 105 | ${ }^{\circ} \mathrm{C}$ |
| T02 | Ts | Permissible Storage Temperature Range |  | -40 |  | 105 | ${ }^{\circ} \mathrm{C}$ |
| T03 | Tpk | Soldering Peak Temperature | tpk < 20 s, convection reflow <br> tpk < 20 s, vapor phase soldering <br> MSL 5A (max. floor life 24 h at $30^{\circ} \mathrm{C}$ and $60 \%$ RH); Refer to Handling and Soldering Conditions for details. |  |  | $\begin{aligned} & 245 \\ & 230 \end{aligned}$ | $\begin{aligned} & { }^{\circ} \mathrm{C} \\ & { }^{\circ} \mathrm{C} \end{aligned}$ |
| T04 | Rthja | Thermal Resistance Chip to Ambient | package mounted on PCB according to JEDEC standard |  |  |  |  |

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## ELECTRICAL CHARACTERISTICS

Operating conditions: VDD $=4.5 \ldots 5.5 \mathrm{~V}, \mathrm{Tj}=-40 \ldots 125^{\circ} \mathrm{C}$, unless otherwise noted

| Item No. | Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Supply |  |  |  |  |  |  |  |
| 001 | VDD | Supply Voltage | referenced to GND | 4.5 | 5.0 | 5.5 | V |
| 002 | I(VDD) | Supply Current | no external load, code disc at specified distance |  | 4 | 8 | mA |
| 003 | I(VDD) | Supply Current | no external load, without code disc |  | 7 | 12 | mA |
| Outputs |  |  |  |  |  |  |  |
| 101 | Isc()hi | Short-Circuit Current high G(4:0) | $V()=$ GND | -55 |  |  | mA |
| 102 | Isc() lo | Short-Circuit Current low G(4:0) | $V()=V D D$ |  |  | 45 | mA |
| 103 | Isc(PB)hi | Short-Circuit Current high PB | $V(P B)=G N D$ | -15 |  |  | mA |
| 104 | Isc(PB)lo | Short-Circuit Current low PB | $\mathrm{V}(\mathrm{PB})=\mathrm{VDD}$ |  |  | 35 | mA |
| 105 | Vs()hi | Saturation Voltage High G(4:0) | Vs() $\mathrm{hi}=\mathrm{VDD}-\mathrm{V}() ; \mathrm{I}()=-1.6 \mathrm{~mA}$ |  |  | 0.4 | V |
| 106 | Vs()lo | Saturation Voltage Low G(4:0) | l()$=1.6 \mathrm{~mA}$ |  |  | 0.4 | V |
| 107 | $\mathrm{Vs}(\mathrm{PB})$ | Saturation Voltage at PB | Full range $\left(\mathrm{V}(\mathrm{PB})=0.1^{*} \mathrm{VDD} \ldots 0.9^{*} \mathrm{VDD}\right)$ $I(P B)= \pm 1 \mathrm{~mA}$ | -25 |  | 25 | mV |
| PowerOn |  |  |  |  |  |  |  |
| 201 | VDDon | Turn-on Threshold VDD (power-on release) | increasing voltage at VDD | 3.9 | 4.2 | 4.4 | V |
| 202 | VDDoff | Turn-off Threshold VDD (power-down reset) | decreasing voltage at VDD | 3.7 | 3.95 | 4.2 | V |
| 203 | VDDhys | Threshold Hysteresis VDD | VDDhys = VDDon - VDDoff | 0.2 | 0.25 | 0.3 | V |
| General |  |  |  |  |  |  |  |
| 301 | tupd | Refresh Rate G(4:0), PB |  |  | 250 | 1000 | $\mu \mathrm{s}$ |
| 302 | n | Maximum Revolution Speed | code disc at specified distance | 300 |  |  | RPM |

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## FUNCTION

The iC-PG is a reflective opto encoder that is ready to use immediately after power-up, as it requires no configuration.

The output interface is a simple parallel 5 -bit position output $\mathrm{G}(4: 0)$, which allows 30 Gray-coded output positions. This allows output positions with a resolution of $12^{\circ}$, including a code for power-off and a code for error monitoring.

An additional analog output is available for monitoring the push-button actuation. The analog signal output provides a voltage proportional to the probe distance between the iC-PG chip and the code disc. The analog output resolves 64 steps in relation to $10 \%$... $90 \%$ of VDD. Because the axial actuation of a push-button is also detected, iC-PG can be used as a complete single-chip encoder for push-button applications. The electronic monitoring of the push-button actuation minimizes the mechanical requirements for the overall structure.

The position output $\mathrm{G}(4: 0)$ is initially "00000" and indicates the power-off state. Since " 00000 " is presented when power is missing, interfacing iC-PG's parallel outputs is simple. After the supply voltage at VDD has exceeded the power-on threshold VDDon, the output $G(4: 0)$ changes to "10000". This is the error state and indicates that no valid position has been found yet.

Once in the "10000" output state, the iC-PG performs the following to update its parallel outputs with the actual rotary position and push-button actuation distance. This update sequence is repeated at the refresh rate tupd:

- 1. Turn on the LED
- 2. Wait for the photodiodes to collect the incoming light
- 3. The photodiode information is latched, evaluated, and transformed into position and distance
- 4. The distance between the iC-PG chip and code disc is output on pin PB
- 5. The rotary knob angle position is output on the parallel outputs $G(4: 0)$

| Position | $\mathbf{G ( 4 )}$ | $\mathbf{G ( 3 )}$ | $\mathbf{G ( 2 )}$ | $\mathbf{G ( 1 )}$ | $\mathbf{G ( 0 )}$ |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Power off | 0 | 0 | 0 | 0 | 0 |
| 1 | 0 | 0 | 0 | 0 | 1 |
| 2 | 0 | 0 | 0 | 1 | 1 |
| 3 | 0 | 0 | 0 | 1 | 0 |
| 4 | 0 | 0 | 1 | 1 | 0 |
| 5 | 0 | 0 | 1 | 1 | 1 |
| 6 | 0 | 0 | 1 | 0 | 1 |
| 7 | 0 | 0 | 1 | 0 | 0 |
| 8 | 0 | 1 | 1 | 0 | 0 |
| 9 | 0 | 1 | 1 | 0 | 1 |
| 10 | 0 | 1 | 1 | 1 | 1 |
| 11 | 0 | 1 | 1 | 1 | 0 |
| 12 | 0 | 1 | 0 | 1 | 0 |
| 13 | 0 | 1 | 0 | 1 | 1 |
| 14 | 0 | 1 | 0 | 0 | 1 |
| 15 | 0 | 1 | 0 | 0 | 0 |
| 16 | 1 | 1 | 0 | 0 | 0 |
| 17 | 1 | 1 | 0 | 0 | 1 |
| 18 | 1 | 1 | 0 | 1 | 1 |
| 19 | 1 | 1 | 0 | 1 | 0 |
| 20 | 1 | 1 | 1 | 1 | 0 |
| 21 | 1 | 1 | 1 | 1 | 1 |
| 22 | 1 | 1 | 1 | 0 | 1 |
| 23 | 1 | 1 | 1 | 0 | 0 |
| 24 | 1 | 0 | 1 | 0 | 0 |
| 25 | 1 | 0 | 1 | 0 | 1 |
| 26 | 1 | 0 | 1 | 1 | 1 |
| 27 | 1 | 0 | 1 | 1 | 0 |
| 28 | 1 | 0 | 0 | 1 | 0 |
| 29 | 1 | 0 | 0 | 1 | 1 |
| 30 | 1 | 0 | 0 | 0 | 1 |
| Error | 1 | 0 | 0 | 0 | 0 |
|  |  | 0 | 0 | 0 |  |

Table 4: Gray Code Assignment

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## SAFETY ADVICE

Depending on the mode of operation, these devices emit highly concentrated visible blue light which can be hazardous to the human eye.

Products which incorporate these devices have to follow the safety precautions given in IEC 60825-1 and IEC 62471.

## HANDLING ADVICE

Because of the specific housing materials and geometries used, these LED devices are sensitive to rough handling or assembly and can thus be easily damaged
or may fail in regard to their electro-optical operation. Excessive mechanical stress or load on the LED surface or to the glass windows must be avoided.

## REVISION HISTORY

| Rel. | Rel. Date | Chapter | Modification | Page |
| :--- | :--- | :--- | :--- | :--- |
| A1 | $2019-04-08$ |  | Initial release | all |


| Rel. | Rel. Date | Chapter | Modification | Page |
| :--- | :--- | :--- | :--- | :--- |
| A2 | $2021-06-15$ |  | Correction of English descriptions | $1-6$ |


| Rel. | Rel. Date $^{*}$ | Chapter | Modification | Page |
| :--- | :--- | :--- | :--- | :--- |
| B1 | $2021-08-18$ | PACKAGES | Revised Package Picture | 1 |
|  |  | PACKAGING INFORMATION | Pin Configuration and Pin Designation changed | 2 |
|  |  | PACKAGE DIMENSIONS | Package Drawing updated | 3 |

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## ORDERING INFORMATION

| Type | Package | Options | Order Designation |
| :--- | :--- | :--- | :--- |
| iC-PG | $8-$ pin optoDFN, <br> $3 \mathrm{~mm} \times 3 \mathrm{~mm}$, <br> 0.9 mm thickness <br> RoHS compliant <br> Kit with Reflective Encoder IC <br> PG1M (61mm x 64 mm $),$ <br> Code Disc <br> Adapter PCB <br> $(80 \mathrm{~mm} \times 110 \mathrm{~mm})$ | iC-PG oDFN8-3x3 <br> Mother board | incl. ribbon cable and iC-PG <br> inlay |

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[^0]:    * Release Date format: $Y Y Y Y-M M-D D$

