iC-MA3 EVAL MA3_1D

EVALUATION BOARD DESCRIPTION

preliminary

ORDERING INFORMATION

Type	Order Designation	Description
iC-MA3	iC-MA3 EVAL MA3_1D	Evaluation board for iC-MA3

BOARD MA3_1D

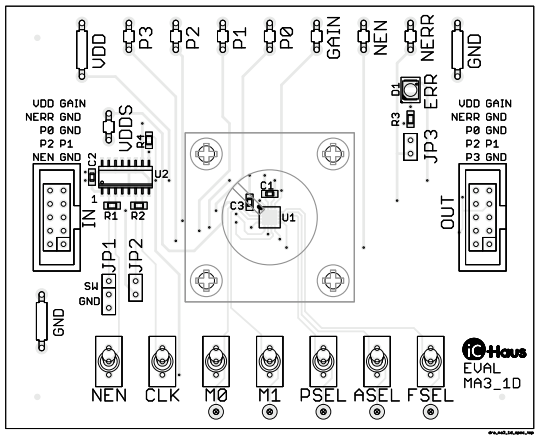

Figure 1: Component side

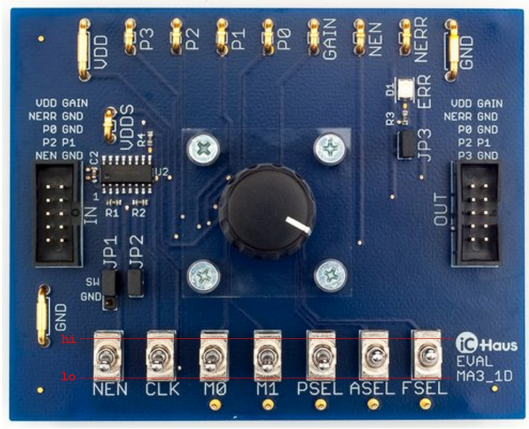

Figure 2: Top view

TERMINAL	DESCRIPTION
VDD	Supply Voltage (+3.0...+5.5 V)
GND	Ground (0 V)
P3	Port 3 / Output NENO
P2	Port 2
P1	Port 1
P0	Port 0 / Input CLK
GAIN	Amplitude Control Gain Output
NEN	Enable Input (active low)
NERR	Error Output (active low)

NEN switch	sets level (hi,lo) for NEN pin
CLK switch	sets level (hi,lo) for P0 pin
M0 switch	sets level (hi,lo) for M0 pin
M1 switch	sets level (hi,lo) for M1 pin
PSEL switch	sets level (hi,open,lo) for PSEL pin
ASEL switch	sets level (hi,open,lo) for ASEL pin
FSEL switch	sets level (hi,open,lo) for FSEL pin
JP1	connects NEN input either to NEN-switch or to GND
JP2	connects P0 pin to (buffered) CLK-switch
JP3	connects NERR output to (buffered) LED
IN	10-pole connector (for daisy-chaining)
Out	10-pole connector (for daisy-chaining)

The evaluation board is delivered with all jumpers JP1, JP2 and JP3 bridged. Depending on application, jumpers should be set or removed accordingly. By rotating the knob, the angular position of the Ø4x4 mm diametral magnet mounted at the lower end of the plastic hollow shaft can be changed freely. With the toggeling switches set in the "up" position, a logic high level is assigned to the corresponding configuration pin. Conversely, a "down" position sets the logic level to low. In additon, the switches PSEL, ASEL and FSEL can be set to a "middle" position, leaving the corresponding pin in an open state.

CIRCUIT SCHEMATIC

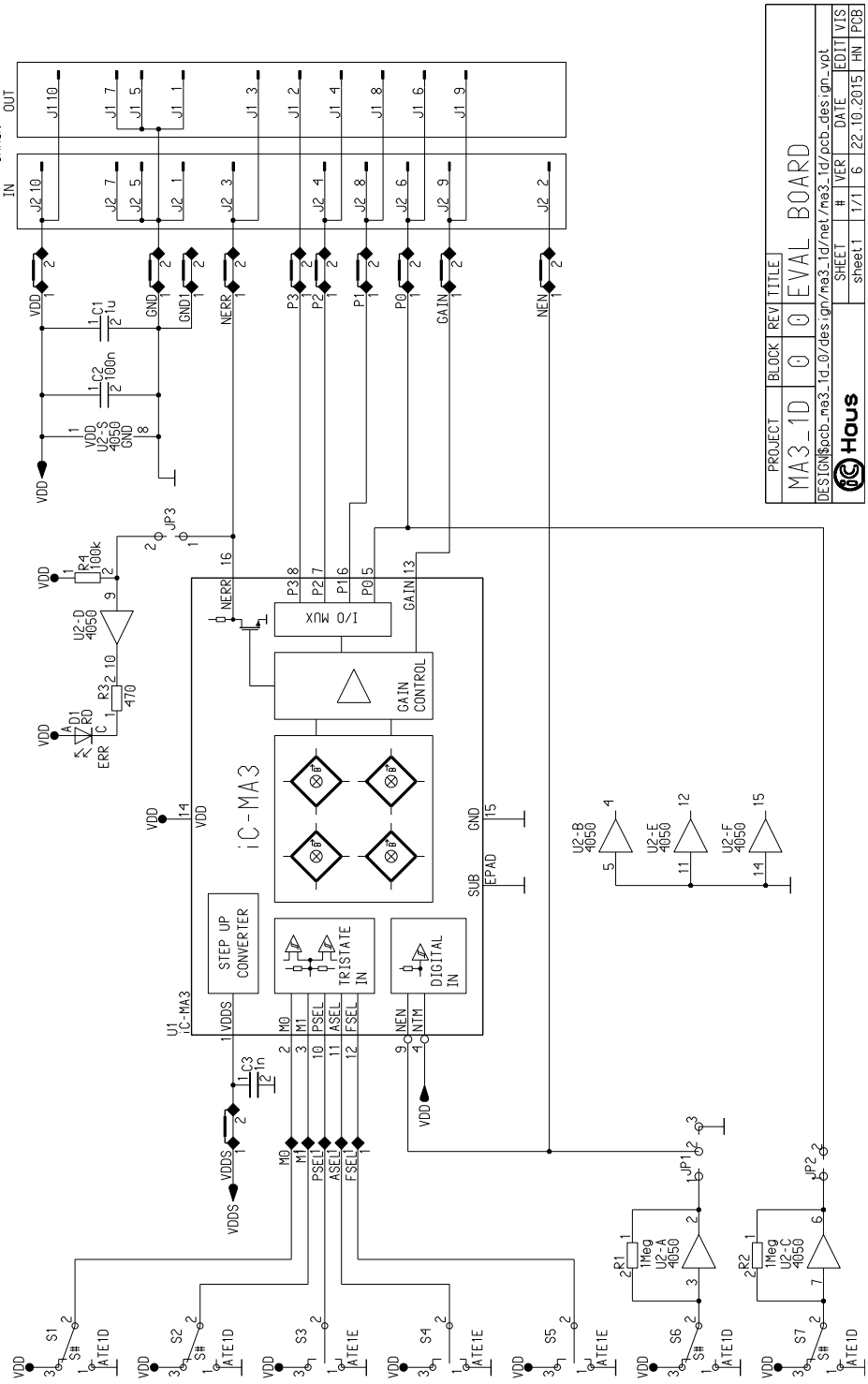

Figure 3: Circuit diagram including optional components

RELATED DOCUMENTS

- iC-MA3 Data Sheet - Specification -

→ http://www.ichaus.de/product/iC-MA3

REVISION HISTORY

Rel.	Rel. Date*	Chapter	Modification	Page
A1	2017-05-17	ALL	Initial release	

Rel.	Rel. Date*	Chapter	Modification	Page
A2	2019-01-04	BOARD MA3_1D	VDD supply voltage description extended to +3.0...5.5V Settings of M0, M1 switches corrected to (hi, lo) Settings of hi, lo positions of the toggle switches indicated in Fig.2 for clarity and explained in the description.	1

* Release Date format: *YYYY-MM-DD*