

iC-MFL EVAL MFL1D

EVALUATION BOARD DESCRIPTION

ORDERING INFORMATION

Type	Order Designation	Description Options
Evaluation Board	iC-MFL EVAL MFL1D	Evaluation Board iC-MFL Ready-to-operate, pin-configurable, with logic N-FET onboard

BOARD MFL1D

(size 100 mm x 80 mm)

TERMINAL DESCRIPTION

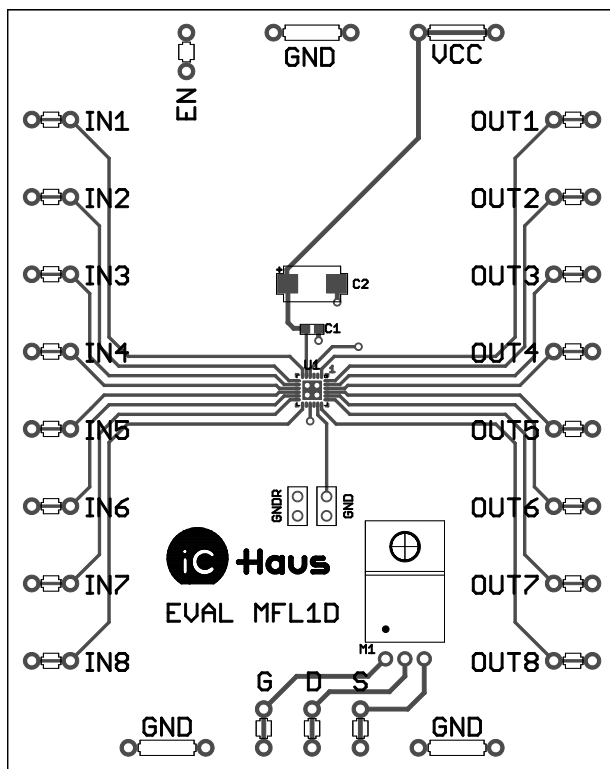


Figure 1: Component side

VCC	+5 V Supply Voltage
GND	0 V Ground
GNDR	0 V Ground (Reference)
IN1	Signal Input 1
IN2	Signal Input 2
IN3	Signal Input 3
IN4	Signal Input 4
IN5	Signal Input 5
IN6	Signal Input 6
IN7	Signal Input 7
IN8	Signal Input 8
OUT1	Output 1
OUT2	Output 2
OUT3	Output 3
OUT4	Output 4
OUT5	Output 5
OUT6	Output 6
OUT7	Output 7
OUT8	Output 8
EN	Enable Input
G	Evaluation FET Gate
D	Evaluation FET Drain
S	Evaluation FET Source

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CONNECTOR AND TERMINAL PINOUT

Input Connector

Name	Function
IN1	Input Channel 1
IN2	Input Channel 2
IN3	Input Channel 3
IN4	Input Channel 4
IN5	Input Channel 5
IN6	Input Channel 6
IN7	Input Channel 7
IN8	Input Channel 8

Output Connector

Name	Function
OUT1	Output Channel 1
OUT2	Output Channel 2
OUT3	Output Channel 3
OUT4	Output Channel 4
OUT5	Output Channel 5
OUT6	Output Channel 6
OUT7	Output Channel 7
OUT8	Output Channel 8

Safe Power Supply

Name	Function
VCC	Power Supply Input
GND	Ground

Enable Input

Name	Function
EN	Enable Input

Evaluation FET

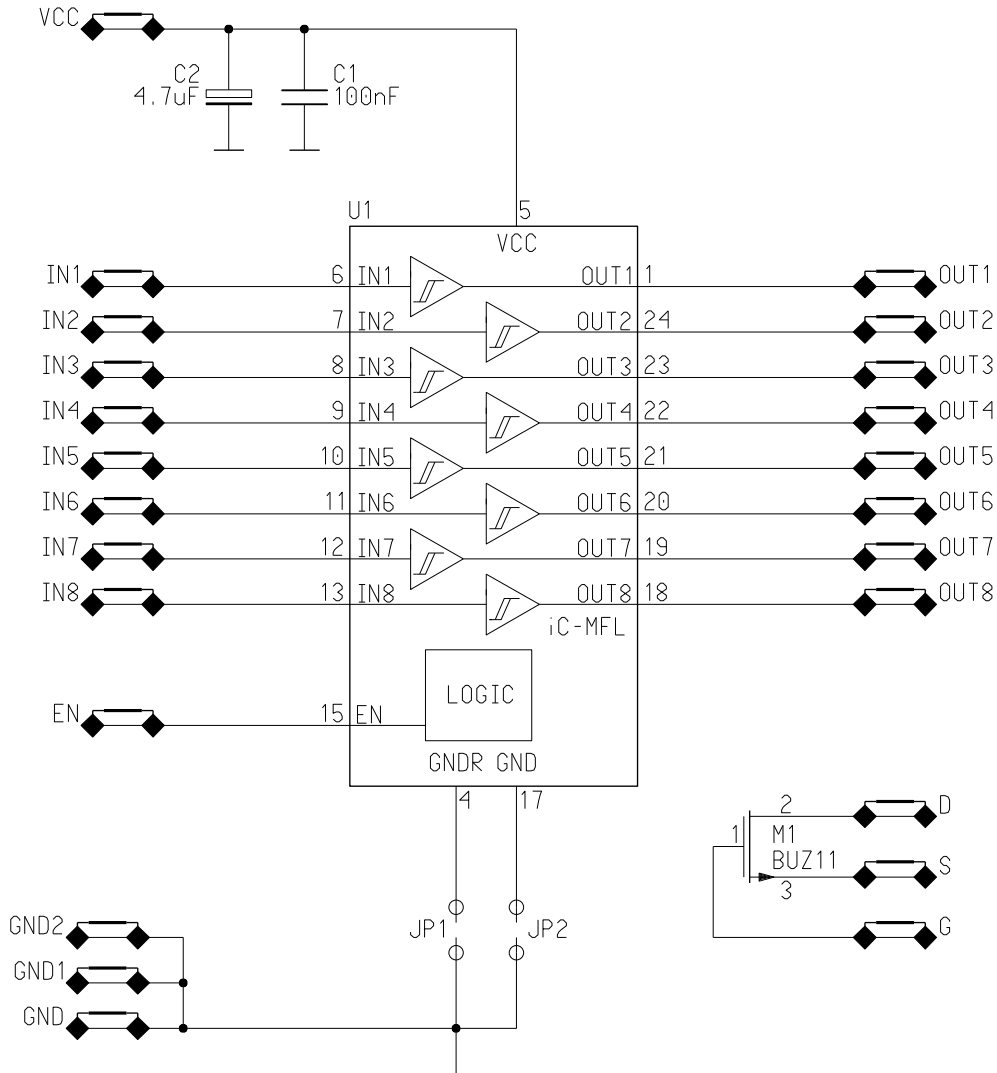
Name	Function
G	Gate Connector
D	Drain Connector
S	Source Connector

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CIRCUIT DESCRIPTION

The MFL1D evaluation board is equipped with the iC-MFL 8-Channel Fail-Safe FET Driver IC. The board features connectors for each IC pin and an evaluation FET for direct use. Power supply failures are directly jumperable.



PROJECT	REV	BLOCK	TITLE			
MFL1D	-	0	Demoboard			
DESIGN CONTEXT \$mf/pcb/mfl1d/mfl1d0/pcb_design_vpt						
	SHEET	#	VER	DATE	EDIT	VIS
	sheet 1	1/1	17	06.07.2006	HN	PCB

Figure 2: Circuit diagram including FET

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JUMPER DESCRIPTION

Closed Jumper	Comments
JP1	Ground Connectivity to GNDR (Reference) (shipment setup)
JP2	Ground Connectivity to GND (shipment setup)

ASSEMBLY PART LIST

Device	Value (typical)	Comment
U1	iC-MFL QFN24	8-Channel Fail-Safe Logic-N-FET Driver
M1	BUZ 11 TO220	N-FET
C1	4.7 μ F	Supply Backup Capacitor
C2	100 nF	Supply Backup Capacitor
JP1, JP2, JP3, JP4	SL LP1 097 2 G	Jumper

REVISION HISTORY

Rel.	Rel. Date*	Chapter	Modification	Page
A1	2017-05-05	All	Initial Release	all

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* Release Date format: YYYY-MM-DD